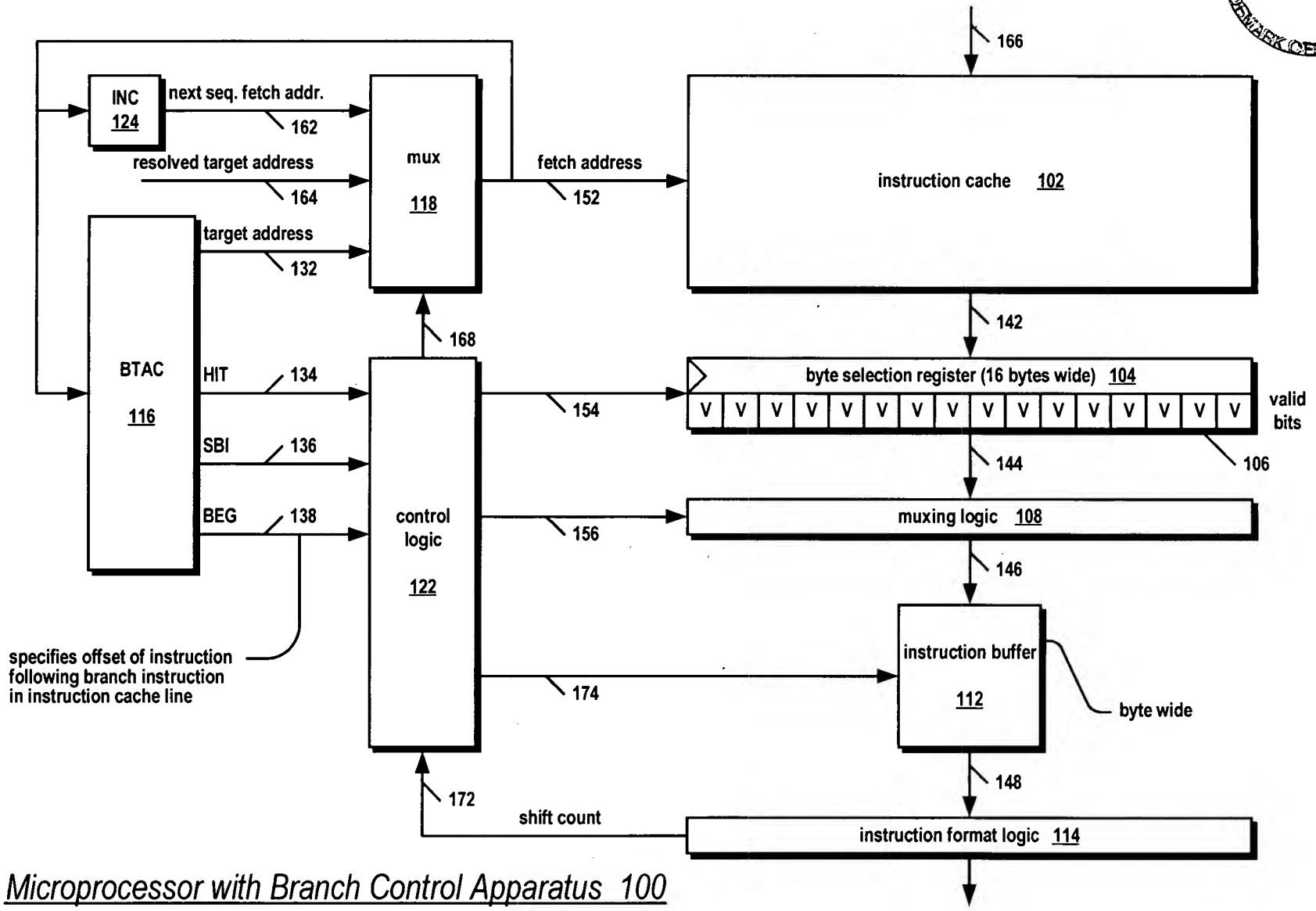
Replacement Sheet

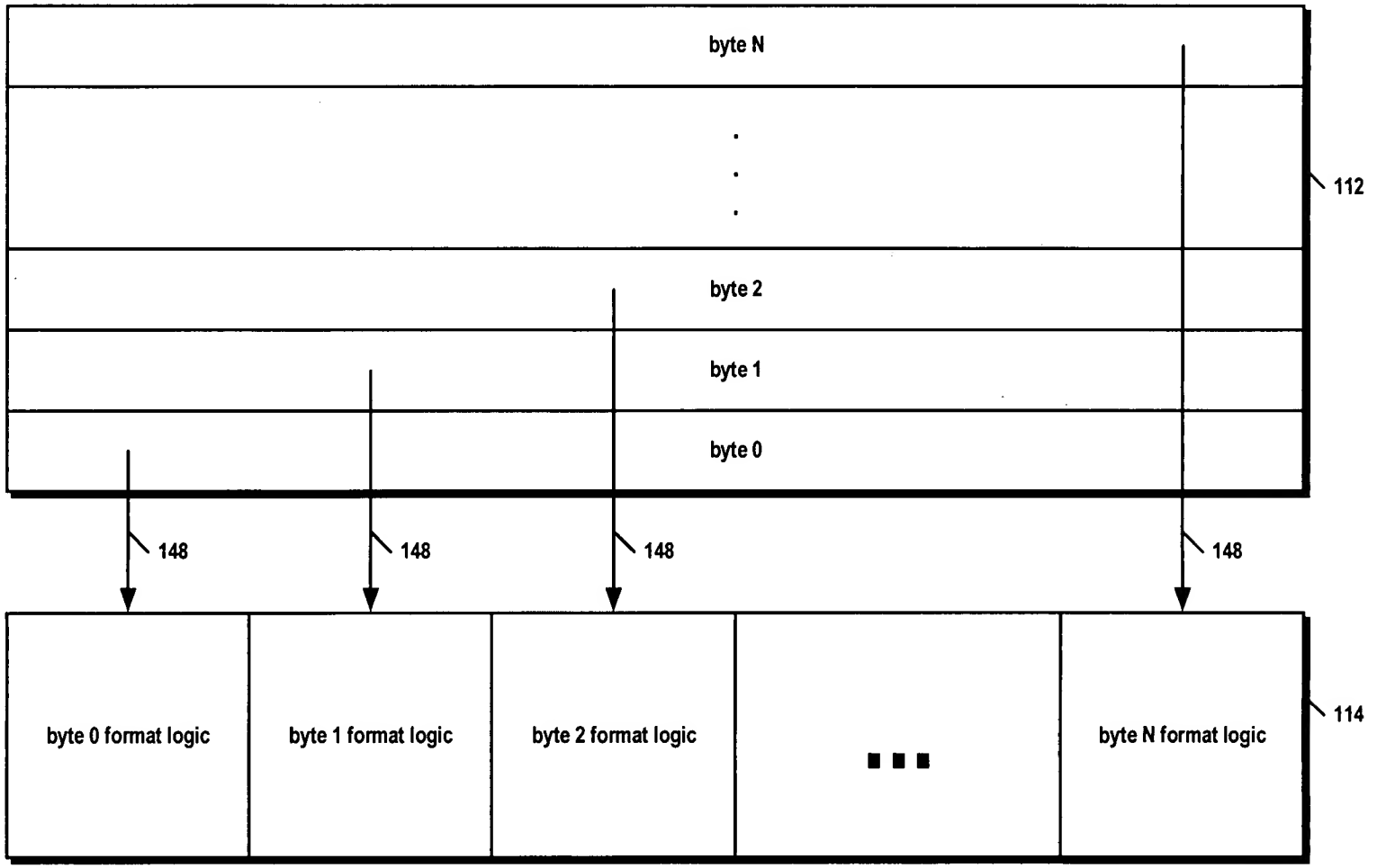
FIG. 1



Microprocessor with Branch Control Apparatus 100

Replacement Sheet

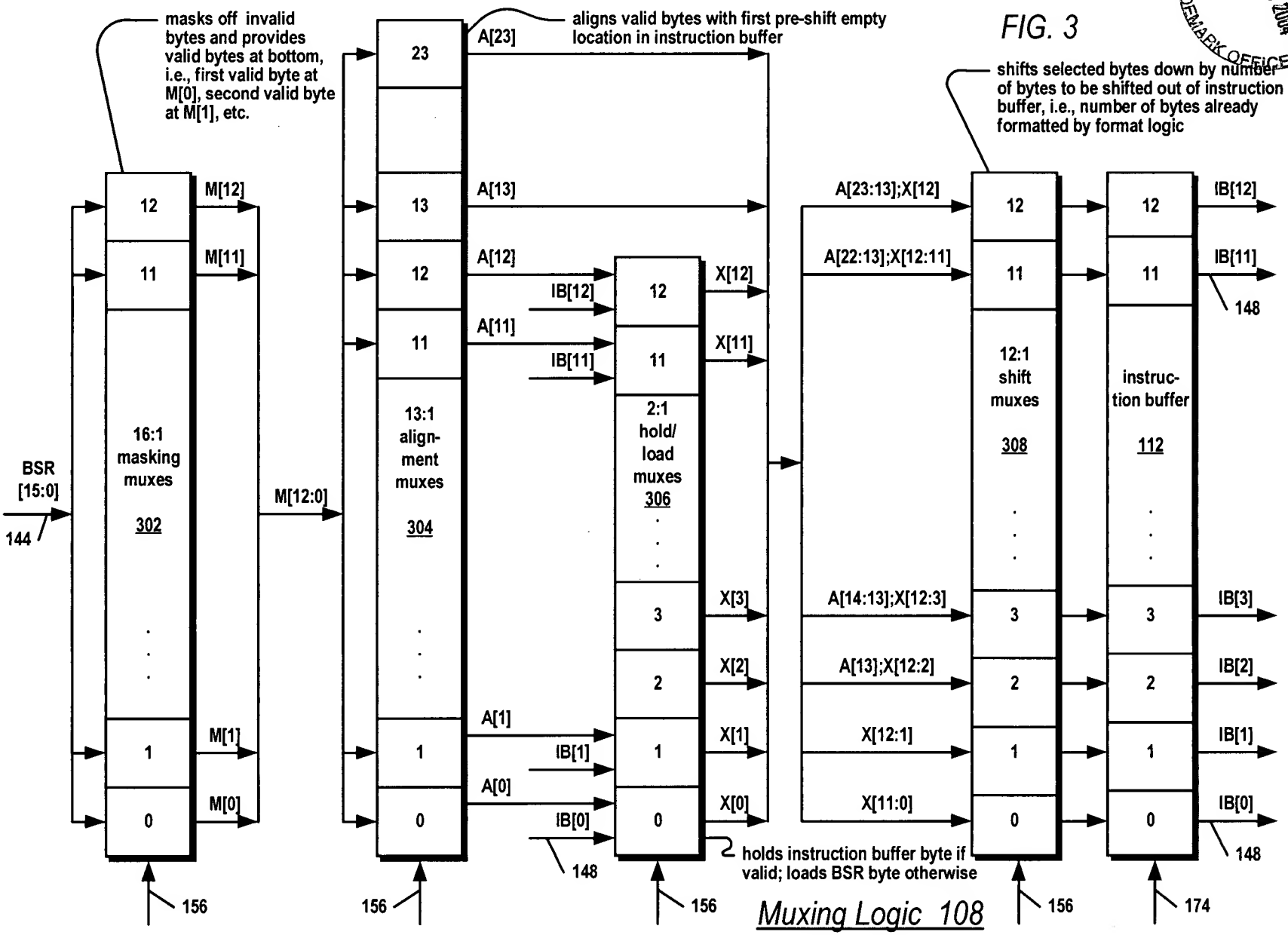
FIG. 2



Instruction Buffer to Instruction Format Logic Coupling 100

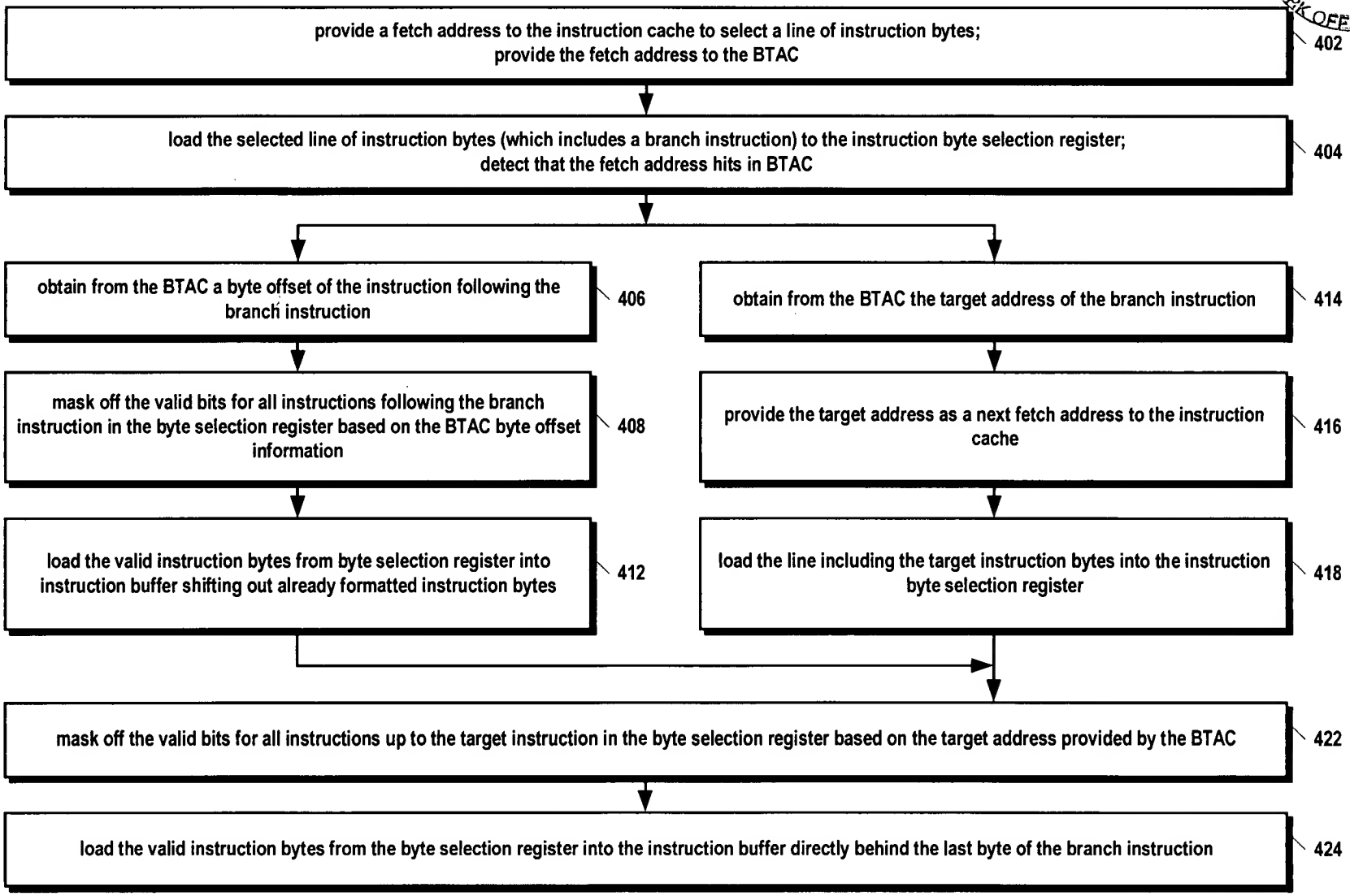
Replacement Sheet

FIG. 3



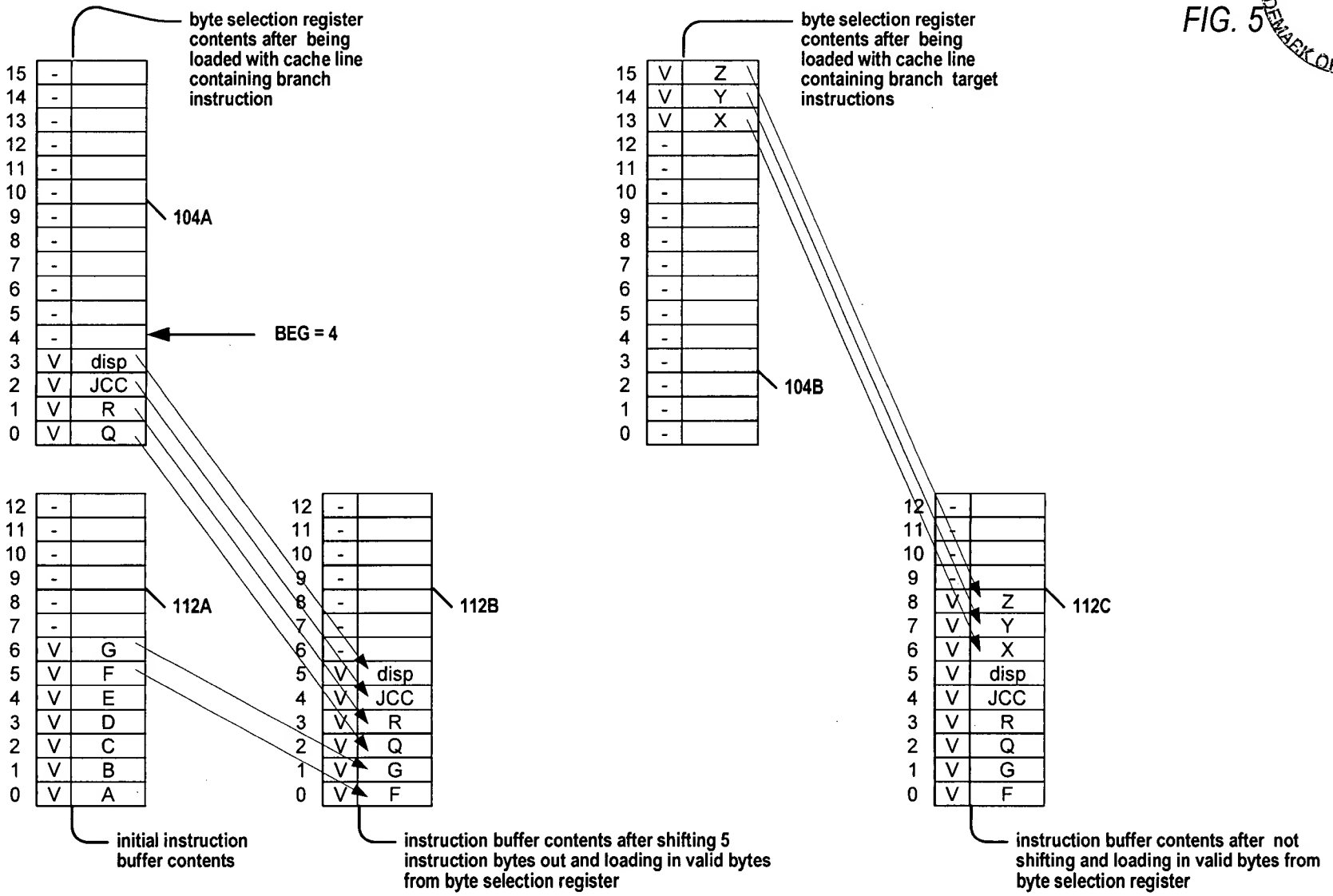
Muxing Logic 108

FIG. 4



Instruction Byte Selection Method

FIG. 5



Branch Control Apparatus Operation Example